

Feedback and oscillators

Unit 4

Q: What is feedback? What are the basic types of feedback?

A: Feedback is the process of taking a part of the output signal and feeding it back to the input. There are two basic types of feedbacks namely 1) positive feedback & 2) negative feedback.

Q: What are positive feedback and negative feedback?

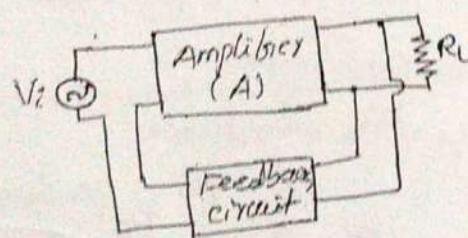
A: positive feedback: Feedback in which a part of the output feedback is in phase with the input. With positive feedback, voltage gain will increase.

Negative feedback: Feedback in which feedback signal is in opposite phase with the input signal. Negative feedback decreases the voltage gain of the amplifier.

Q: Mention with block diagrams, the four types of negative feedback connections.

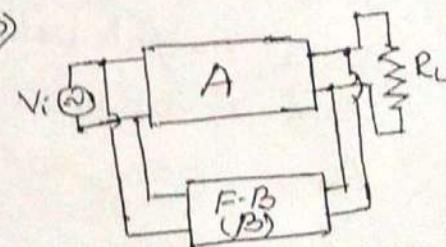
A:

a)



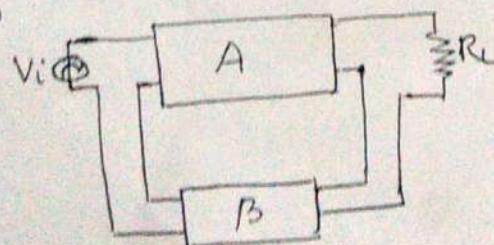
Voltage Series feedback

b)



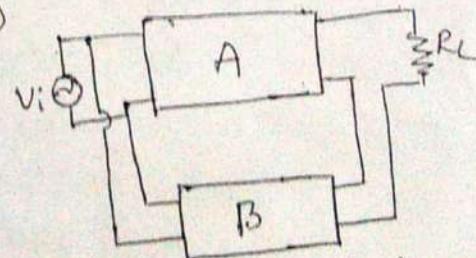
Voltage Shunt feedback

c)



current Series feedback

d)



current Shunt feedback

①

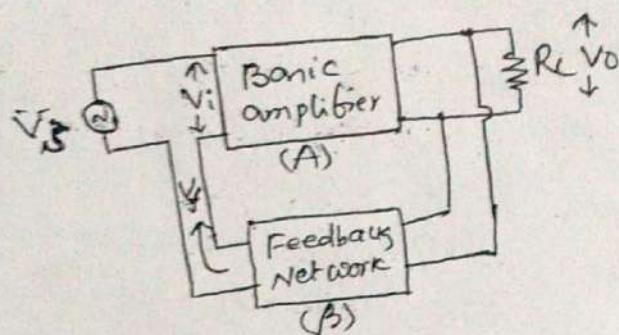
Q: Mention the advantages of negative feedback

A: Advantages of negative feedback are

- Stability in gain
- Increase in input impedance
- Decrease in output impedance
- Decrease in distortion and noise
- Increase in Bandwidth.

Q: Obtain the expression for voltage series negative feedback using block diagram.

A:



VS = Source voltage

Vi = Net input to the basic amplifier

Vf = Feedback voltage

B = Feedback factor

VO = Output voltage

A = Gain of the basic amplifier

AF = Gain with negative feedback

$$D.K.T \quad A = \frac{V_o}{V_i} \quad \text{and} \quad A_F = \frac{V_o}{V_s}$$

But $V_i = V_s - V_f$

$$V_i = V_s - B V_o$$

$$V_i = V_s - B A V_i$$

$$(V_i + A B V_i) = V_s$$

$$V_i [1 + A B] = V_s$$

$$\therefore A_F = \frac{V_o}{V_i [1 + A B]} = \frac{A}{1 + A B}$$

$$[\because A = \frac{V_o}{V_i}]$$

②

The voltage gain with feedback

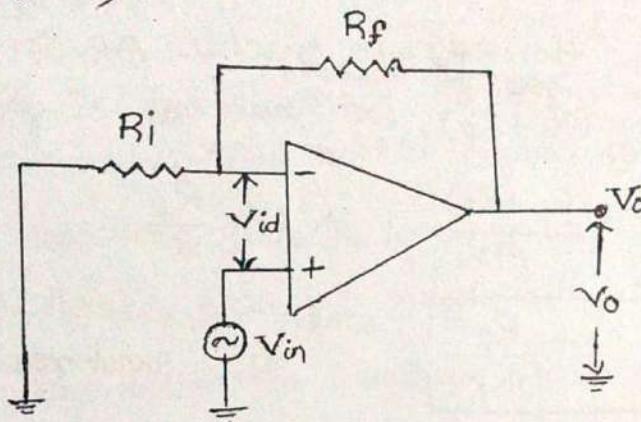
$$A_F = \frac{A}{1+AB}$$

Note: The expression of voltage gain for positive feedback is

$$A_F = \frac{A}{1-AB}$$

Q: Derive the expression for the voltage gain of a noninverting amplifier (Voltage Series feedbacks amplifier).

A:



$$\text{open loop voltage gain} = A = \frac{V_o}{V_{id}} \rightarrow (1)$$

$$\& \text{closed loop voltage gain} = A_F = \frac{V_o}{V_{in}} \rightarrow (2)$$

The differential input to the basic amplifier $V_{id} = V_{in} - V_f$

$$\text{Feedback Voltage } V_f = \frac{R_f}{R_i + R_f} \times V_o \rightarrow (3)$$

$$\begin{aligned} \therefore V_{id} &= V_{in} - \frac{R_f}{R_i + R_f} V_o \\ &= V_{in} - \frac{R_f}{R_i + R_f} A V_{id} \end{aligned} \quad [\because V_o = A V_{id}] \quad \text{from Eq (1)}$$

$$V_{id} + \frac{R_f}{R_i + R_f} A V_{id} = V_{in}$$

$$V_{id} \left[1 + \frac{A R_f}{R_i + R_f} \right] = V_{in} \rightarrow (4)$$

(3)

Substituting Equation (4) in equation (2) we have

$$\begin{aligned}
 AF &= \frac{V_o}{V_{id}} \left[1 + \frac{AR_1}{R_1 + R_f} \right] \\
 &= \frac{V_o}{R_1 + R_f + AR_1} \times \frac{R_1 + R_f}{V_{id}} \\
 AF &= \frac{(R_1 + R_f) A}{R_1 + R_f + AR_1} \quad \rightarrow (5)
 \end{aligned}$$

This is the exact expression for the voltage gain with negative feedback. However in practice $AR_1 \gg R_1 + R_f$.

Therefore neglecting $(R_1 + R_f)$. we have

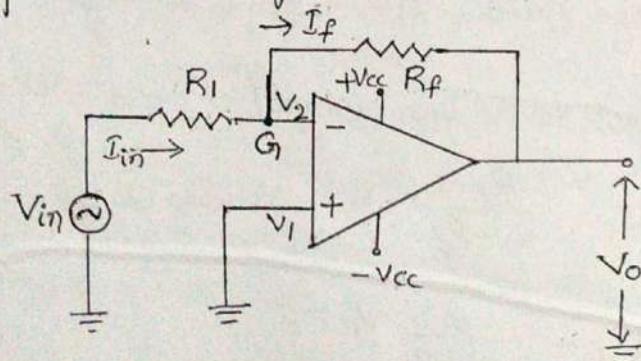
$$AF = \frac{(R_1 + R_f) A}{AR_1} = 1 + \frac{R_f}{R_1}$$

$$AF = 1 + \frac{R_f}{R_1}$$

where R_f = Feedback resistor
 R_1 = Input resistor.

Q: Derive the expression for voltage gain of Inverting amplifier [Voltage Shunt feedback amplifier]

A:



Applying KCL to input node V_2 we have

$$I_{in} = I_f + I_B \rightarrow (1) \text{ But } I_B \approx 0$$

$$\therefore I_{in} \approx I_f \quad \rightarrow (2)$$

$$\text{ie } \frac{V_{in} - V_2}{R_1} = \frac{V_2 - V_o}{R_f} \rightarrow (3)$$

(5)

W.R.T $A = \frac{V_o}{V_{id}}$ where $V_{id} = V_1 - V_2$

But $V_1 = 0$ and $V_{id} = -V_2$

$$\therefore A = \frac{V_o}{-V_2} \Rightarrow V_2 = -\frac{V_o}{A}$$

Substituting V_2 in equation (3) we get

$$\frac{V_{in} + \frac{V_o}{A}}{R_i} = -\frac{\left(\frac{V_o}{A}\right) - V_o}{R_f}$$

$$\therefore A_F = \frac{V_o}{V_{in}} = -\frac{A R_F}{R_i + R_f + A R_i} \rightarrow (4)$$

Equation (4) is called exact equation and -ve sign indicates the input and output signals are out of phase by 180° . However, since A is very large, $A R_i \gg R_i + R_f$ therefore $(R_i + R_f)$ can be neglected in Eq(4)

Therefore expression for voltage gain of inverting amplifier is $A_F = -\frac{A R_F}{A R_i}$

$$AF = -\frac{R_F}{R_i} \quad \boxed{\text{Ideal equation.}} \rightarrow (5)$$

Q: What is virtual ground?

A: For an ideal op-amp, the open loop gain is very high and approaches infinity (∞)

i.e. Differential gain $A_d = \frac{V_o}{V_{id}} \rightarrow \infty$

This means $V_{id} = (V_1 - V_2) \rightarrow 0$, when the non-inverting input is grounded ($V_1 = 0$), the voltage at the inverting input (V_2) must be 0. Therefore the V_2 point in the inverting amplifier is called virtual ground since it is not directly connected to ground.

⑤

B: Discuss the effect of negative feedback on input resistance, output resistance, distortion noise, and bandwidth.

A: Effect of -ve feedback on input resistance (R_i):-

Negative feedback increases the input resistance of the amplifier R_i by a factor of $(1+A\beta)$. i.e

$$\text{Input resistance with feedback } R_{if} = R_i [1+A\beta]$$

Effect of -ve feedback on output resistance (R_o):- -ve feedback decreases the output resistance of the amplifier R_o by a factor of $(1+A\beta)$

$$\text{i.e. output resistance with feedback } R_{of} = \frac{R_o}{(1+A\beta)}$$

Effect of -ve feedback on distortion noise:- -ve feedback decreases distortion and noise of the amplifier by a factor of $(1+A\beta)$. Therefore

$$\text{Distortion with feedback } D_f = \frac{D}{(1+A\beta)}$$

where D is distortion without feedback.

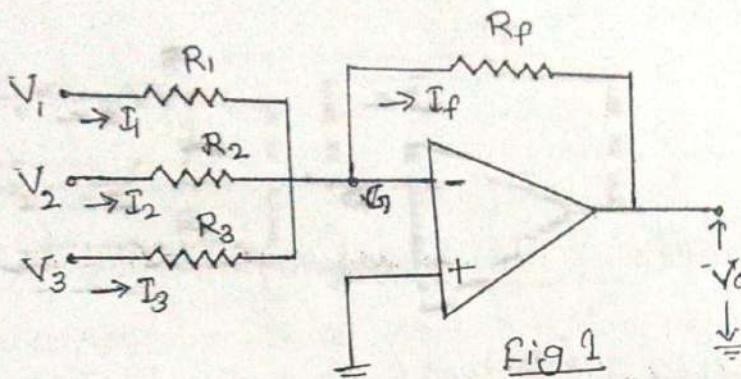
$$\text{Hence Noise with feedback } N_f = \frac{N}{1+A\beta}$$

Effect of -ve feedback on Bandwidth :- -ve feedback increases the bandwidth of the amplifier ($B\omega$) by a factor of $(1+A\beta)$.

$$\text{Hence Bandwidth with feedback } (B\omega)_f = (B\omega)[1+A\beta]$$

Op-amp Applications

- Q: What is an adder? Obtain the expression for its output.
- A: An adder is a circuit, whose output is proportional to the algebraic sum of the input voltages. Below figure shows the circuit diagram of inverting adder.



Let I_1, I_2, I_3 be the three input currents set by the input voltages V_1, V_2 and V_3 respectively. Let I_f be the feed-back current in R_f .

$$\text{From KCL, we have } I_1 + I_2 + I_3 = I_f \rightarrow (1)$$

$$\text{where } I_1 = \frac{V_1}{R_1}, I_2 = \frac{V_2}{R_2} \text{ and } I_3 = \frac{V_3}{R_3} \text{ and } I_f = -\frac{V_o}{R_f}$$

$$\therefore \frac{V_1}{R_1} + \frac{V_2}{R_2} + \frac{V_3}{R_3} = -\frac{V_o}{R_f} \rightarrow (2)$$

$$\text{or } V_o = - \left[\frac{R_f}{R_1} V_1 + \frac{R_f}{R_2} V_2 + \frac{R_f}{R_3} V_3 \right]$$

If $R_1 = R_2 = R_3$ then

$$V_o = - \frac{R_f}{R} [V_1 + V_2 + V_3] \rightarrow (3)$$

The sign is due to the inverting mode of op-amp

Thus the output voltage is proportional to the algebraic sum of the three inputs.

Note: Scaling or weighted amplifier: - If each input voltage is amplified by a different factor in an adder, the circuit is then called a scaling or weighted amplifier.

$$\text{Thus the output voltage } V_o = - \left[\frac{R_f}{R_1} V_1 + \frac{R_f}{R_2} V_2 + \frac{R_f}{R_3} V_3 \right]$$

$$\text{where } \frac{R_f}{R_1} \neq \frac{R_f}{R_2} \neq \frac{R_f}{R_3}$$

$$\text{or } V_o = - \left[K_1 V_1 + K_2 V_2 + K_3 V_3 \right]$$

$$\text{where } \frac{R_f}{R_1} = K_1 \\ \frac{R_f}{R_2} = K_2 \text{ and } \frac{R_f}{R_3} = K_3$$

(7)

Average circuit :- In a circuit in which output voltage is equal to the average of all the input voltages. Figure 1 shown in adder can be used as an averaging circuit.

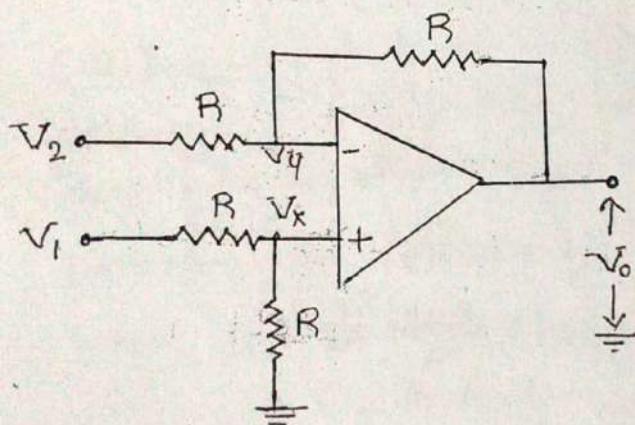
$$\text{Consider o/p } V_o = - \left[\frac{R_f}{R_1} V_1 + \frac{R_f}{R_2} V_2 + \frac{R_f}{R_3} V_3 \right]$$

Q6 $R_1 = R_2 = R_3$ and $\frac{R_f}{R} = \frac{1}{3}$ we have

$$V_o = - \left[\frac{V_1 + V_2 + V_3}{3} \right]$$

Q: What is a Subtractor? obtain the expression for its output.

A: A Subtractor or differential amplifier is a circuit, whose output is equal to the difference between the input signals.



From Superposition theorem, the output voltage is the algebraic sum of output V_{o1} due to inverting input voltage V_2 and output V_{o2} due to noninverting input voltage V_x

$$\text{i.e. } V_o = V_{o1} + V_{o2} \rightarrow (1)$$

V_{o1} is the output due to inverting input voltage V_2 only. Therefore from inverting amplifier theory ~~$A = -\frac{R_f}{R_1}$~~ or

$$V_o = -\frac{R_f}{R_1} V_i \quad V_{o1} = -\frac{R}{R} V_2 \rightarrow (2)$$

And V_{o2} is the output voltage due to noninverting input voltage V_x only. Hence from noninverting amplifier theory

$$\left[A = \left(1 + \frac{R_f}{R_1} \right) \text{ or } V_o = \left(1 + \frac{R_f}{R_1} \right) V_i \right]$$

$$V_{o2} = \left(1 + \frac{R_f}{R} \right) V_x \quad \text{where } V_x = \left(\frac{R}{R+R} \right) V_1$$

$$\therefore V_{O2} = \left(1 + \frac{R}{R}\right) \left(\frac{R}{R+R}\right) V_1$$

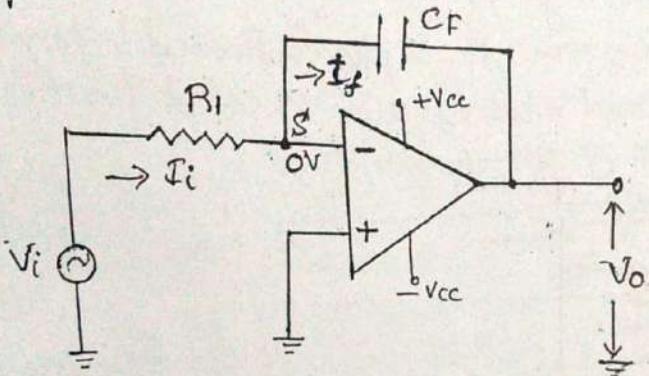
$$V_{O2} = V_1$$

$$\therefore \text{From Eq (1) we have } V_O = -\frac{R}{R} V_2 + V_1$$

or \$V_O = V_1 - V_2\$ thus output voltage is the difference of two inputs.

Q: What is an integrator? Obtain the expression for the output of integrator. Sketch the ~~output~~ waveforms.

A: A circuit in which output is proportional to integral of input is called an integrator.



Applying KCL to the node S, we have

$$I_i = I_f \rightarrow (1)$$

$$\text{But } I_i = \frac{V_i - 0}{R_I} \quad \text{and} \quad I_f = -C_F \frac{d(V_o - 0)}{dt} \quad \text{--- (2)}$$

$$\therefore \frac{V_i}{R_I} = -C_F \frac{dV_o}{dt} \quad \text{--- (4)}$$

Integrating Eq (4) on both sides, we get

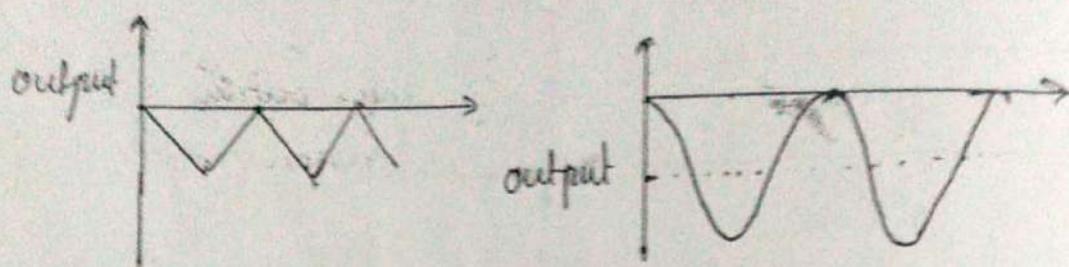
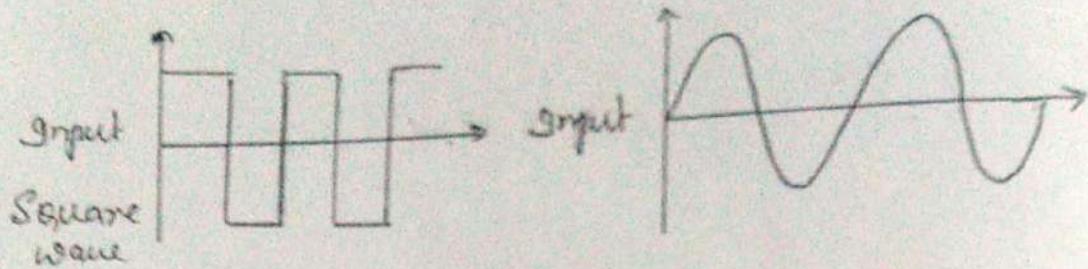
$$\int \frac{V_i}{R_I} dt = -C_F \int \frac{dV_o}{dt} dt$$

$$\Rightarrow \boxed{V_o = -\frac{1}{R_I C_F} \int_0^t V_i dt + C} \quad \text{--- (5)}$$

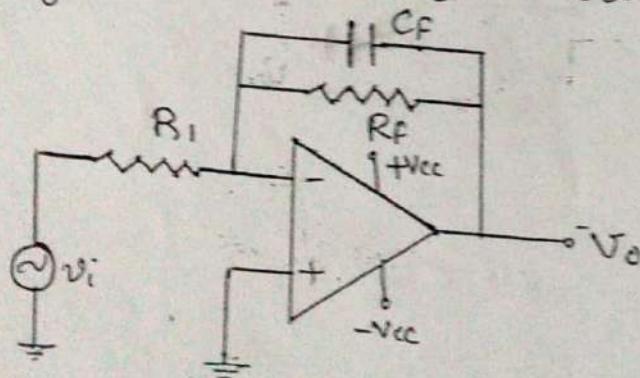
where C is the constant of integration.

Equation (5) is the expression for output

(9)



Note:- practical integrator: In the integrator circuit shown above, the gain tends towards infinity at very low frequencies. As a result any small input voltage introduce an error voltage at the output. To minimise this error, a feedback resistor R_f is connected in parallel with the capacitor and this circuit is called practical integrator which is given below.



The limiting frequency at which the gain is 0 dB is

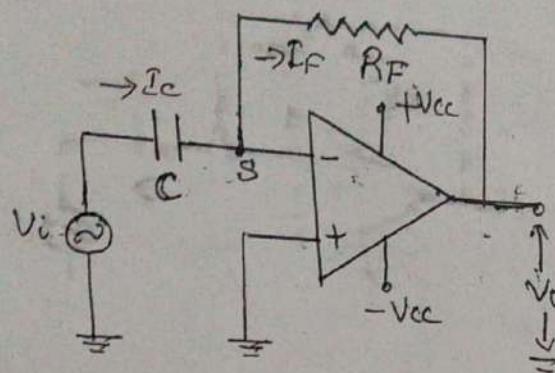
$$f_1 = \frac{1}{2\pi R_1 C_F}$$

The upper limiting frequency at which the gain decreases at a rate of 20 dB/decade is

$$f_2 = \frac{1}{2\pi R_F C_F}$$

Q: What is a differentiator? Obtain the expression for the output. Sketch the waveforms.

A: A circuit in which output voltage is proportional to the derivative of the input voltage is called a differentiator.



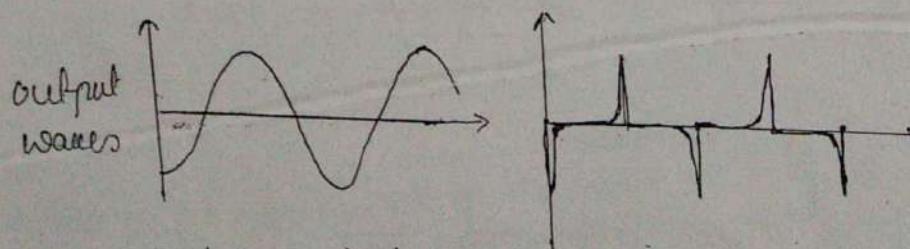
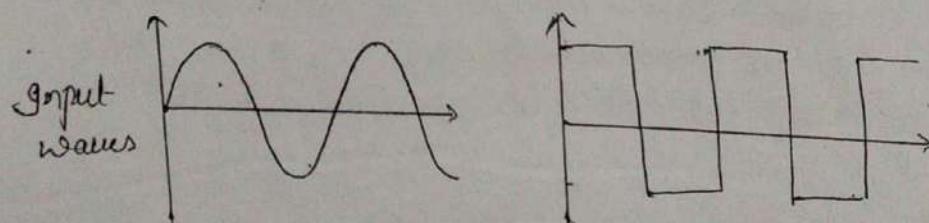
Applying KCL to the node S, we have $I_C = I_F \rightarrow (1)$

$$\text{But } I_C = C \frac{d}{dt} (V_i - 0) = C \frac{d}{dt} V_i \quad \text{and } I_F = \frac{0 - V_o}{R_F} = -\frac{V_o}{R_F}$$

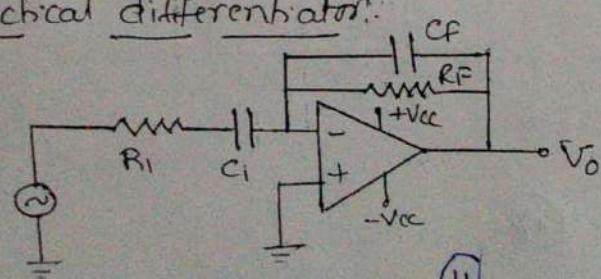
$$\therefore C \frac{d(V_i)}{dt} = -\frac{V_o}{R_F}$$

$$\text{or } V_o = -R_F C \frac{d}{dt} V_i$$

Waveforms:



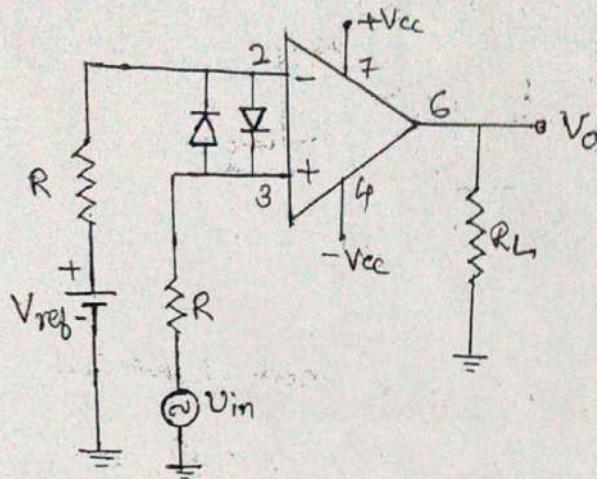
Practical differentiator:



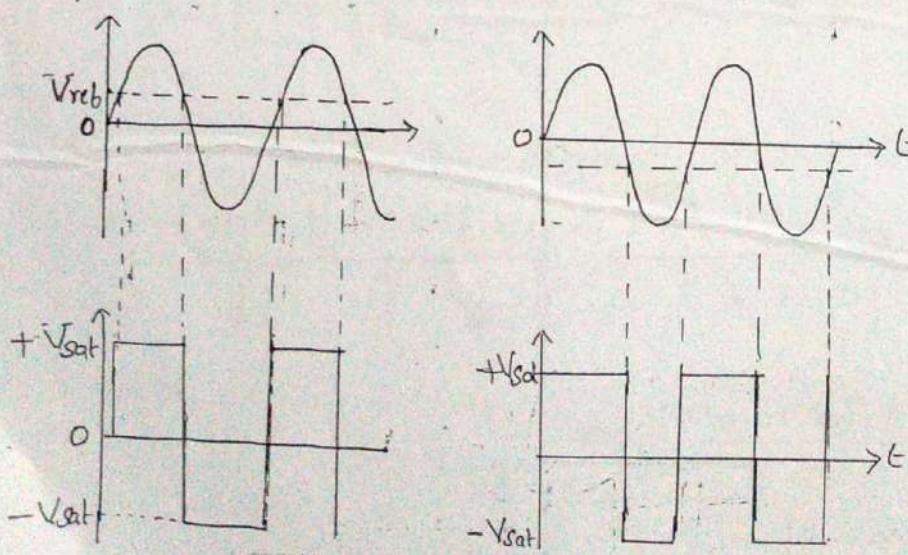
(11)

Q: What is a comparator? Explain the working of an op-amp comparator

A: Comparator is a circuit which compares a signal voltage on one input with a known voltage called the reference voltage on the other input.



A fixed reference voltage V_{ref} is applied to the negative input of the op-amp. The time dependent input signal is applied to the positive input. When V_{in} is less than V_{ref} the output voltage V_o will be at $-V_{sat}$. This is because the voltage at the negative input is higher than that at the positive input. When V_{in} is greater than V_{ref} the output V_o goes to $+V_{sat}$. In this manner V_o changes from one saturation level to another whenever $V_{in} = V_{ref}$. The comparator is also called as a voltage level detector.



Note: characteristics of a Comparator

- 1) Speed of operation
- 2) Accuracy 3) compatibility of the output.

Q: what is a Schmitt trigger?. Explain the working of op-amp Schmitt trigger.

A: A Schmitt trigger is a circuit which converts any kind of waveform into a rectangular wave.

A comparator using a positive feedback becomes Schmitt trigger.

When the output voltage is negatively saturated a negative voltage gets fed back to the noninverting input. This holds the output in the low state. When the output is positive saturated, a positive voltage gets fed back to the noninverting input. This positive feedback holds the output in the high state. The feedback fraction is

$$\beta = \frac{R_1}{R_1 + R_2}$$

When the output is saturated fully the reference voltage applied to the noninverting input is

$$V_{ref} = +\beta V_{sat}$$

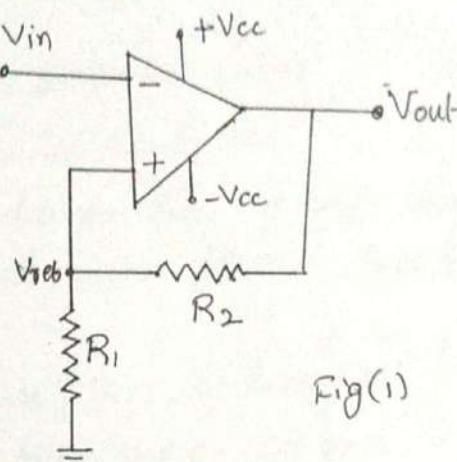
When the output is negatively saturated the reference voltage is

$$V_{ref} = -\beta V_{sat}$$

The figure 2 is called a hysteresis curve. When the input crosses the reference voltage the output switches from $+V_{sat}$ to $-V_{sat}$. When the input goes below the negative reference voltage the output switches back to $+V_{sat}$. The difference between the two reference voltages gives the hysteresis voltage.

$$\text{Hysteresis voltage} = \beta [+V_{sat} - (-V_{sat})] = 2\beta V_{sat}$$

(13)



Fig(1)

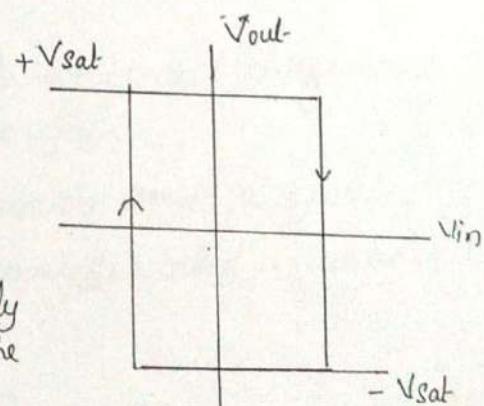


Fig 2

Active Filters

Q: What is a filter circuit? mention the different types of filters.

A: A filter is a frequency selective circuit which passes a specified band of frequencies and attenuates or blocks frequencies outside this band. Filters may be classified in a number of ways as follows.

1. Analog or digital filters
2. Passive or active filters
3. Audio (AF) or Radio (RF) frequency filters.

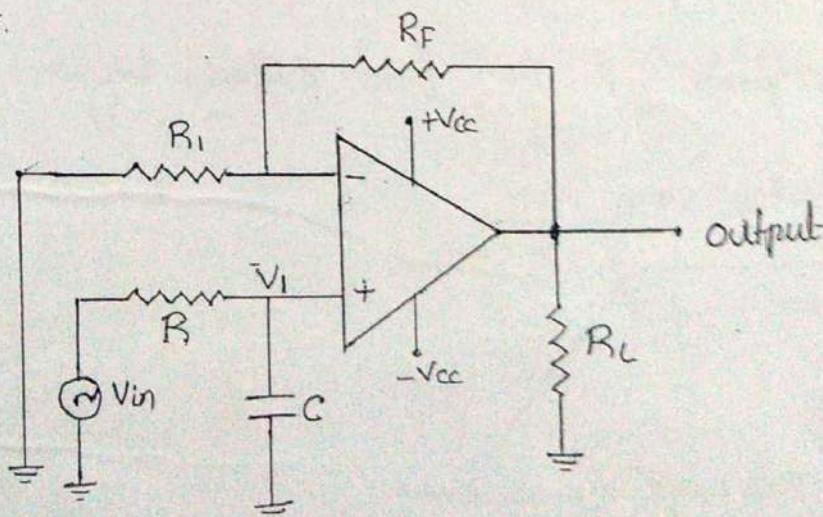
Q: what are active filters? mention the types.

A: Filters which employ active devices like op-amps along with passive elements are called active filters. The different types of ~~are~~ filters are,

- a) Low pass filter
- b) High pass filter
- c) Band pass filter
- d) Band stop or band reject filter and e) All pass filters.

Q: what is low pass filter? with the circuit diagram obtain the expression for voltage gain.

A: A filter which passes low frequencies up to a specific frequency known as cut off frequency f_c is called a low pass filter.



In the circuit, op-amp is used in the noninverting configuration. Hence resistors R_1 & R_F determine the gain of the filter.

According to the voltage divider rule, the voltage at the noninverting terminal is

$$V_1 = \frac{-jX_C}{R-jX_C} V_{in}$$

where $J = \sqrt{-1}$ and $-jX_C = \frac{1}{j2\pi f C}$

Therefore $V_1 = \frac{V_{in}}{1+j2\pi f RC}$ and the output voltage

$$V_o = \left(1 + \frac{R_F}{R_1}\right) V_1$$

$$V_o = \left(1 + \frac{R_F}{R_1}\right) \frac{V_{in}}{1+j2\pi f RC}$$

$$\frac{V_o}{V_{in}} = \frac{AF}{1+j(f/f_H)}$$

Where $\frac{V_o}{V_{in}}$ = gain of the filter

$AF = 1 + \frac{R_F}{R_1}$ pass band gain of the filter

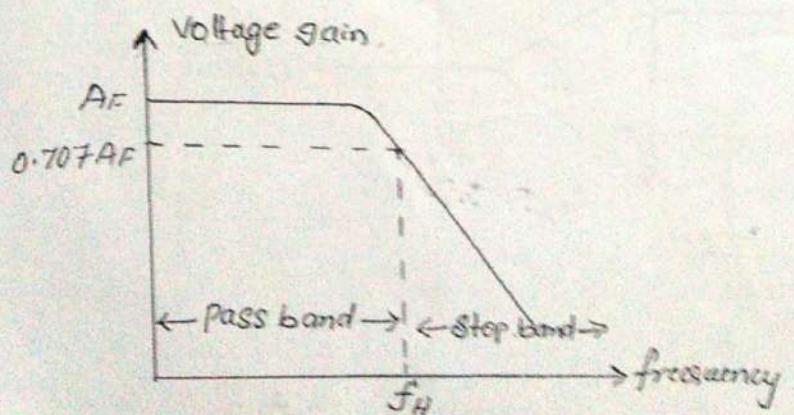
$f_H = \frac{1}{2\pi RC}$ = high cutoff frequency of the filter

At very low frequencies $f < f_H$, $|\frac{V_o}{V_{in}}| \approx AF$

At $f = f_H$ $|\frac{V_o}{V_{in}}| = \frac{AF}{\sqrt{2}} = 0.707AF$

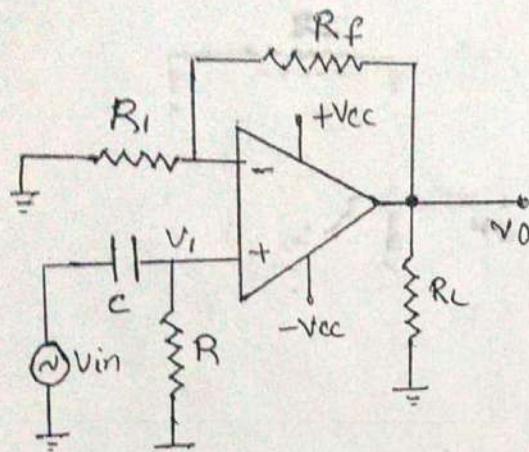
At $f > f_H$ $|\frac{V_o}{V_{in}}| < AF$

Frequency response of low pass filter is shown below

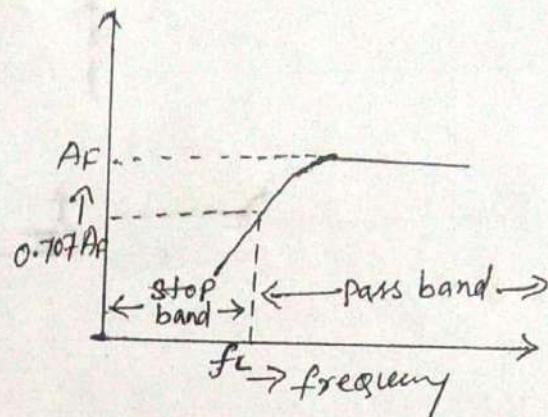


A: what is a high pass filter? write circuit diagram and obtain expression for voltage gain.

A: A filter which allows all high frequencies above a specific frequency known as cut off frequency f_L is called a high pass filter.



(1) circuit diagram



(2) Frequency response

For the above circuit of noninverting mode, the output voltage is

$$V_0 = \left(1 + \frac{R_f}{R_1}\right) \frac{j2\pi f R C}{1 + j2\pi f R C} V_{in}$$

$$\left[\because V_1 = \frac{R}{R + \left(\frac{1}{j\omega C}\right)} V_{in} = \frac{j2\pi f R C}{1 + j2\pi f R C} V_{in} \right]$$

Substituting $\frac{1}{2\pi R C} = f_L$ in the above equation we have

$$V_0 = \left(1 + \frac{R_f}{R_1}\right) \frac{j\left(\frac{f}{f_L}\right)}{1 + j\left(\frac{f}{f_L}\right)} V_{in}$$

$$\text{But } \left(1 + \frac{R_f}{R_1}\right) = AF$$

$$\therefore \frac{V_0}{V_{in}} = AF \left[\frac{j\left(\frac{f}{f_L}\right)}{1 + j\left(\frac{f}{f_L}\right)} \right]$$

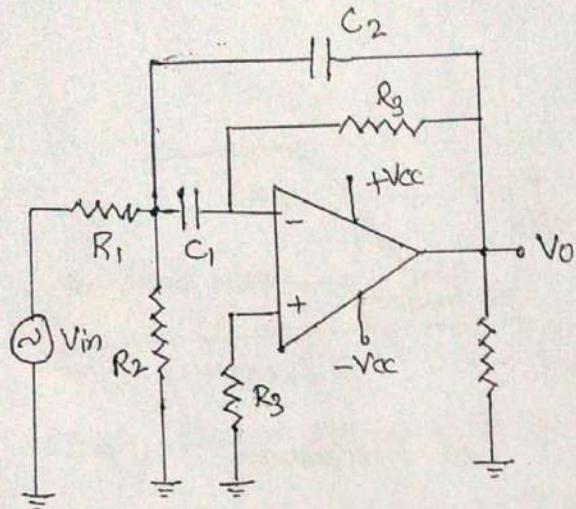
where $1 + \frac{R_f}{R_1} = AF = \text{pass band gain}$. $f = \text{frequency of the input signal}$, $f_L = \frac{1}{2\pi R C} = \text{low cut off frequency}$

$$\text{and } \left| \frac{V_0}{V_{in}} \right| = \frac{AF \left(\frac{f}{f_L}\right)}{\sqrt{1 + \left(\frac{f}{f_L}\right)^2}}$$

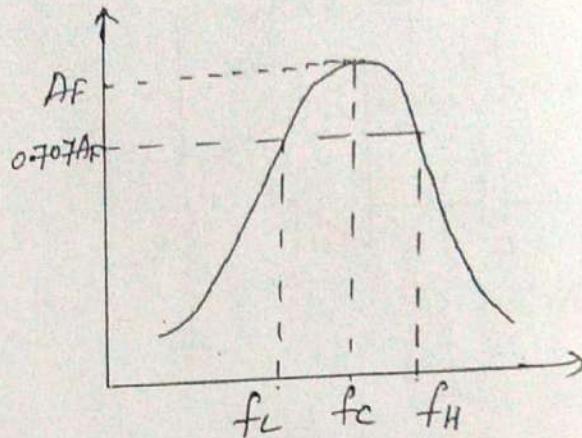
(16)

Q: What is bandpass filter? Write circuit diagram and frequency response of narrow band pass filter.

A: A filter which passes the signal between two cutoff frequencies f_L & f_H and attenuates or stops the other frequencies.



circuit diagram



Frequency response

Q: what are band stop and All pass filters?

A: A filter which stops or attenuates a specific band of frequencies and allows the other frequencies is called as a bandstop or band reject filter.

A filter which passes all frequency components of the input signal providing predictable phase shifts for the different frequencies